

Improvement on Step Response in Steam Distillation Process through of Fuzzy Based Controller Integrated PID Anti-Windup

Haslizamri Md Shariff, Mohd Hezri Fazalul Rahiman*, Mazidah Tajjudin, Mohd Hezri Marzaki, Mohd Hafiz A. Jalil and Ku Siti Syahidah Ku Mohd Noh

Abstract- The Proportional-Integral-Derivative (PID) controller is widely used in control systems due to its ease of development, tuning, efficiency, and reliable. However, it has a significant drawback known as the wind-up phenomenon, which occurs when the integrator action does not account for input saturation. This can lead to high overshoot and longer settling times, as the controller struggles to unwind the control input signal. Additionally, PID controllers often struggle to manage the nonlinear dynamics of Small-scale Steam Distillation Systems. This paper aims to develop a Fuzzy-based controller with an integrated anti wind-up strategy to improve system response. The Fuzzy controller is chosen due to its ability in regulate the nonlinear dynamics of the system. The anti wind-up strategy helps reduce overshoot and improve settling time, ensuring better temperature regulation. Simulation results demonstrate that the proposed Fuzzy-based controller integrated anti wind-up significantly outperforms the conventional PID controller. The Fuzzy integrated anti wind-up controller achieves an overshoot of less than 1%, while the PID controller exceeds 16%. Furthermore, the Fuzzy integrated anti wind-up controller has a settling time of under 4000 seconds, compared to over 7700 seconds for the PID controller.

Index Terms—Proportional-Integral-Derivatives integrated Anti-Windup (PIDAW), Hybrid Fuzzy-PID (HFPID), Hybrid Fuzzy-PID Anti-Windup (HF-PIDAW).

I. INTRODUCTION

Since the 1930s, PID controllers have been widely used in industrial processes because they are easy to tune, efficient, and reliable [1]–[6]. According to [7], hundreds of tuning methods

have been developed over the years to improve PID performance. However, no single method has been officially proven to be the best. A major drawback of classical PID controllers is integral windup, which happens due to input saturation—a common nonlinear behavior in control systems.

This occurs when the controller's output exceeds the actuator's limits, often during large disturbances or set-point changes. When this happens, the integral part of the PID keeps accumulating error even though the actuator can't respond anymore, causing the control signal to grow excessively.

As a result, the system may experience large overshoots, longer settling times, or even instability [8]–[11]. This issue is worsened when the controller incapable unwind the excessive signal once the actuator comes back within its operating range. Essentially, the feedback loop becomes ineffective, and the controller continues to respond incorrectly.

Input saturation and wind-up are common problems in nonlinear systems, and must be carefully considered when designing PID controllers to ensure stable and efficient closed-loop performance [8], [12], [13]. In steam distillation process, the PID controller are commonly utilized in order to regulate the whole process [14]–[18]. However, only a few number of research of development of PID controller are focus and consider the windup phenomenon problem in steam distillation system [19]–[21]. Until today, the engineers and practitioners only focusing to regulate the process of steam of distillation column only without consider encountered wind-up phenomena [10], [21].

The classical PID controller has limitations, particularly in handling the windup phenomenon, which occurs due to system nonlinearities caused by control input constraints [10]. This issue is linked to the integral action of the PID, which keeps accumulating error even when the actuator has reached its limits. As a result, the controller generates signals that exceed the actuator's operating range, leading to integral windup [22], [23]. This breaks the feedback loop and degrades system performance, causing problems like high overshoot, steady-state error, and instability [10], [24], [25]. To address this, a PID controller with Anti Wind-up (PIDAW) has been developed. While it reduces wind-up effects, it may still fall short in minimizing overshoot and settling time. Therefore, fuzzy-based controllers, such as Hybrid Fuzzy-PID (HF-PID) and Hybrid Fuzzy-PID with Anti-Windup (HF-PIDAW), have been introduced. Comparative studies show that HF-PIDAW

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Haslizamri Md Shariff and Ku Siti Syahidah Ku Mohd Noh is currently a lecturer in the Centre of Foundation Studies, Universiti Teknologi MARA, Dengkil Campus Selangor Malaysia.

Mohd Hezri Fazalul Rahiman and Mazidah Tajjudin are currently a senior lecturer with the Faculty of Electrical Engineering, Universiti Teknologi MARA, Malaysia.

Mohd Hezri Marzaki is currently a lecturer in the Wawasan Open University Penang, Malaysia.

Mohd Hafiz A. Jalil is currently a senior lecturer in the Universiti Tun Hussein Onn Johor Malaysia.

*Corresponding author
Email address: hezrif@uitm.edu.my

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performs better than HF-PID, especially in reducing overshoot and settling time.

This paper proposes using the HF-PIDAW controller, tuned with the Integral Square Error (ISE) method, to overcome

II. USING THE TEMPLATE PID AND PID ANTI-WINDUP STRUCTURE

The PID is the feedback controller that consist of three elements (as shown in Figure 1) knowns as Proportional action, P , Integral action, I and Derivative action, D . The PID structure is shown in Equation (1)

$$u(t) = K_p e(t) + \int_0^t K_i e(t) dt + K_d \frac{de(t)}{dt} \quad (1)$$

Where the K_p , K_i and K_d are the proportional gain, integral gain and derivative gain respectively. Those gains is able to express as in Equation (2), (3) and (4)

$$K_p = K_p \quad (2)$$

$$K_i = \frac{K_p}{T_i} \quad (3)$$

$$K_d = K_p T_d \quad (4)$$

Where the T_i and T_d indicates the integral time constant and derivative time constant.

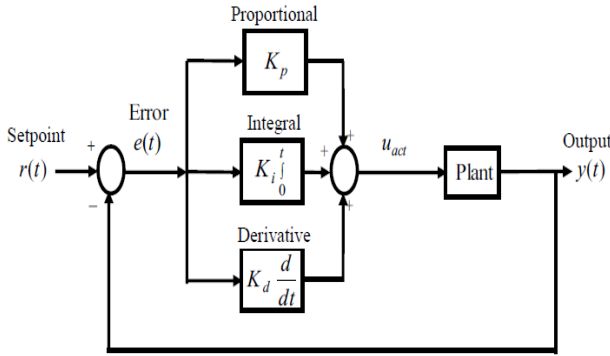


Fig. 1. Basic PID Controller Block Diagram

The PID controller is able to control a system response at a desired set point so that there is no error between output signal and desired set point. The PID parameter's have their own function where the proportional gain gives output which is proportional to current error, $e(t)$. It compares between reference signal versus output value and; resulting error is multiplied with proportional constant to obtain the output signal. Due to limitation of proportional gain, the integral action is needed to improve the steady state error. The integral action will improve the steady state error so that the output is close to desired set point. Meanwhile, derivative action is to improve the system response by anticipating future behavior of the error.

windup issues. The anti wind-up mechanism integrated into the controller helps reduce the negative effects of input nonlinearities and improves closed-loop system performance [26].

The PID is well-known and engineers still applied the controller in production floor due to easy to operate and design. Unfortunately, the PID has some limitation especially once a system is nonlinear in dynamic behavior. In addition, the limitation of actuator in order to regulate integral action at saturate region.

In this research, the basic PID structure will be integrated with additional feedback (as shown in Figure 2); known as anti wind-up function. The functionality of additional feedback which developed in PID structure is to determine the discrepancy value produced of saturated control signal and unsaturated/actual control signal, u_{act} .

The wind-up problem start to attack system dynamic once actual control input penetrate beyond the saturated region. The improvement is done by anti wind-up integrated with PID controller which; an error between saturated signal and actual signal will be notified to new integrator via $1/T_a$ gain to recompute the new integral action value.

The T_a value will continuously compute until the value of T_a form an actual control signal and system dynamic stand at desire set point.

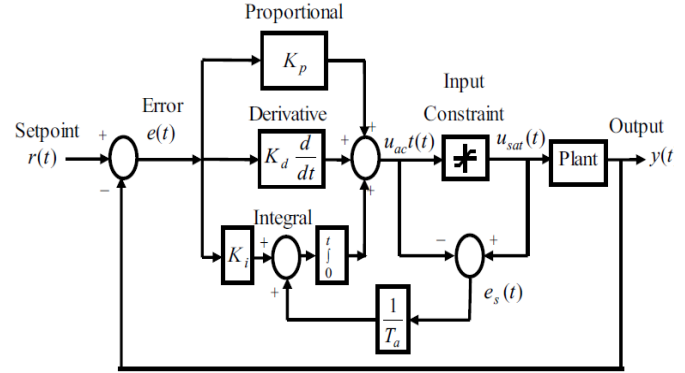


Fig. 2. PID Integrated Anti-Windup (PIDAW) Controller Block Diagram.

To ensure the controller has high performance; the selection of Tracking Time Constant, T_a value is important [45]. The T_a value highly effect on new integrator feedback to reset faster or not and, this phenomenon indirectly effect on overall controller performance [46]. In [47] has suggested the range selection of Tracking Time Constant, T_a value as in Equation (5)

$$T_i \geq T_a \geq T_d \quad (5)$$

So, referring to the Equation (5), determination of T_a values as in Equation (6), (7) and (8)

$$T_a = T_d \quad (6)$$

$$T_a = T_i \quad (7)$$

$$T_a = \sqrt{T_i T_d} \quad (8)$$

The advantage of selection T_d value as smallest value which is $T_a = T_d$; will contribute the fastest reset time to the integrator [29]. The values of new T_a will be applied as new integrator feedback for PID Integrated Anti Wind-up strategies. The compensator will be developed using Tracking Time Constant values based on Equation (6), (7) and (8). Each compensator will be evaluated using transient response and performance indexes to determine the best controller.

II. HFPID AND HFPIDAW STRUCTURE

Today, conventional PID controller integrated with fuzzy logic is widely applied to enhance the robustness of the classical structure of PID. The integration of PID and fuzzy logic controllers, the PID gain were automatically tuned from the output of fuzzy error information. The basic configuration of the fuzzy system as described in Figure 3.

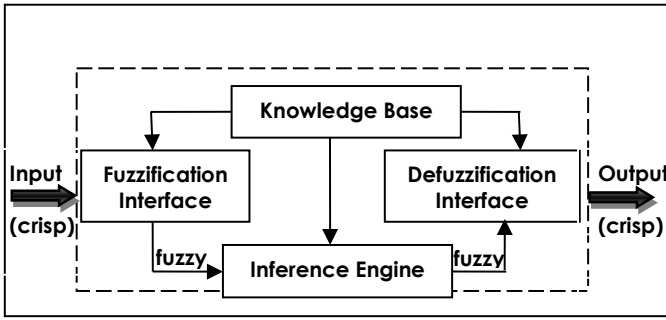


Fig. 3. Configuration of Fuzzy System

The function of fuzzy logic controller is supervise the PID algorithm in attempting to be tuned within the range. All the gains tuned following a set of fuzzy rules in order to achieve the optimum performance. In this study, the CHR tuning rule as a reference to build the range of PID parameters. And, the designing of Membership Function (MF) and fuzzy rules were built based on experimental experience and knowledge which is depend on the plant to be regulated.

A. Parameters of Hybrid Fuzzy-PID Controller

The Mamdani inference system is applied in order to develop the HFPID and, two approaches have involved which were five and seven (Membership Function (MF)). The 5MF and 7MF have couple of input variables which are error, $e(t)$ and derivative of error, de/dt . The three elements of K_p , K_i and K_d parameters are act as the output variable of the HFPID control that then will be fed to each of the PID controller parameter.

Figure 4 illustrates the use of trapezoidal and triangular membership functions (MFs), which were chosen for all linguistic variables in designing the 5MF and 7MF fuzzy systems. One of the main challenges in designing a fuzzy system is selecting the right MFs, as they form the core of the controller. The proper arrangement and shape of MFs greatly affect the system's performance. Currently, there is no universal or best method for determining the ideal MF shapes—they are usually chosen based on the specific problem domain. However, triangular and trapezoidal MFs are the most commonly used, especially in applications involving fuzzy logic controllers, as shown in Figure 4. Triangular MFs are often preferred due to their simplicity and low computational

cost.

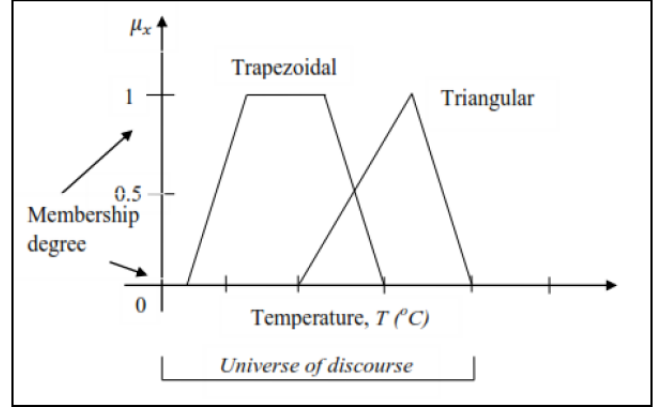


Fig. 4. Shape of Triangular and Trapezoidal MF

TABLE I. FUZZY RULES MAPPING

Derivative of Error	Error				
	NB	NS	ZE	PS	PB
NB	NB	NB	NM	NS	ZE
NS	NB	NS	NS	ZE	PM
ZE	NM	NS	ZE	PS	PB
PS	NS	ZE	PS	PS	PB
PB	ZE	PM	PB	PB	PB

The fuzzy rules are developed by mapping the behavior of the system to be controlled. Each rule consists of two inputs (antecedents) and one output (consequence), based on an input-output relationship. A total of 49 if-then rules were created, as summarized in Table I. In this study, the PID parameters K_p , K_i , K_d are defined within the ranges $[K_p \min K_p \max]$, $[K_i \min K_i \max]$ and $[K_d \min K_d \max]$ respectively. These ranges were determined through PID control simulations to ensure effective and feasible rule bases with high interference efficiency. The parameter values can be normalized within the interval $[0,1][0,1][0,1]$ as shown in Equations (9), (10), and (11).

$$K'p = \frac{K_p - K_{pmin}}{K_{pmax} - K_{pmin}} \quad (9)$$

$$K'i = \frac{K_i - K_{imin}}{K_{imax} - K_{imin}} \quad (10)$$

$$K'd = \frac{K_d - K_{dmin}}{K_{dmax} - K_{dmin}} \quad (11)$$

II. THE SIMULATION OF STEAM DISTILLATION PROCESS

MATLAB 2018 was used to develop a simulation model of the steam distillation process. The system's transfer function was derived from the process reaction curve, which was obtained from the actual operation of a small-scale steam distillation setup. This curve reflects the system's dynamic behavior and provides three key parameters: gain (k), dead time (θ), and time constant (τ).

The steam distillation experiment was carried out at the Distributed Control System (DCS) Laboratory, Faculty of

Electrical Engineering, UiTM Shah Alam. The small-scale steam distillation plant, was designed to extract essential oils using steam distillation. The setup includes several components: a 30 cm diameter stainless steel distillation tank, a water-cooled copper condenser, two resistive temperature detectors (RTDs) for measuring steam and water temperatures, a control panel, and a data acquisition unit. The stainless steel distillation column has a maximum capacity of 25 liters. During oil extraction, raw materials are placed inside this column.

TABLE II.	TRACKING TIME CONSTANT, T_a VALUE
Controller Name	Tracking Time Constant, T_a Value
PIDAW-Td	374
PIDAW-TiTd	488
PIDAW-Ti	638

The simulation study begins with testing both the PID and PIDAW controllers, focusing on step response, setpoint tracking, and load disturbance performance. The PIDAW controller is evaluated with various values of the tracking time constant, T_a , to identify the optimal value that yields the best performance for each tuning.

Next, a comparative study is conducted using advanced intelligent controllers, specifically fuzzy-based controllers, including HF-PID and HF-PIDAW. The HF-PIDAW controller is also tested with different T_a values to determine the most effective tracking time constant for optimal performance.

TABLE III. HFPIDAW CONTROLLER TRACKING TIME CONSTANT, T_a VALUE AND MF

Controller Name	Tracking Time Constant, T_a	T_a Value	Membership Function (MF)
HF-PIDAW - T_i -5MF	T_i	638	5
HF-PIDAW- $\sqrt{T_i T_d}$ -5MF	$\sqrt{T_i T_d}$	488	
HF-PIDAW- T_d -5MF	T_d	374	
HF-PIDAW- T_i -7MF	T_i	638	7
HF-PIDAW- $\sqrt{T_i T_d}$ -7MF	$\sqrt{T_i T_d}$	488	
HF-PIDAW- T_d -7MF	T_d	374	

In this research, the development of PIDAW will be divided to three difference parameters of Tracking Time Constant, T_a . The PIDAW were tested with three differences of Tracking Time Constants, T_a values as in Table II.

While, HFPIDAW controllers are developed based on difference values of Tracking Time Constant, T_a where all given name with suffix depend on T_a and MF applied for that controller. The name of the controllers is listed in Table III.

III. SIMULATION RESULTS OF PID AND PIDAW CONTROLLERS

Figure 5 illustrates the step response of PIDAW-ISE-. T_d The analysis of the responses is as provided in Table IV. Based on the percentage of overshoot, the smallest overshoot is contributed by PIDAW-ISE- T_d . This percentages overshoot is 0.63% lower as compared with PIDAW-ISE- $\sqrt{T_i T_d}$ and 1.47% smaller overshoot as compared with PIDAW-ISE- T_i . Unfortunately, the PID-ISE contribute the highest overshoot as compared to other PIDAW-ISE controllers with 26.72%. In term of rise time, the PIDAW-ISE- T_d PIDAW-ISE- T_i and PIDAW-ISE- $\sqrt{T_i T_d}$ have the fastest rising time with 1405 seconds as compared to PID-ISE controller.

The result for settling time, T_s indicate that the PIDAW-ISE- T_d provides the fastest settling time and only took 7773 seconds to reach its settling time. It is 11 s faster as compared with PIDAW-ISE- $\sqrt{T_i T_d}$ and 41 s faster than PIDAW-ISE- T_i controller. Unfortunately, the PID-ISE is required 12684 s to reach the settling time which is more than 4000 s slower than PIDAW-ISE controllers. In term of steady state analysis, the result show that PIDAW-ISE- T_d provides the smallest IAE, ITAE and ISE index and PID-ISE contribute the highest of IAE, ITAE and ISE index as compared to other controllers. The root cause of higher overshoot and longer settling time of PID-ISE compared to others PIDAW-ISE due to PID-ISE is unintegrated with anti-windup strategy which the controller unrealized that the integral part reach the saturation point and continuously integrate an error.

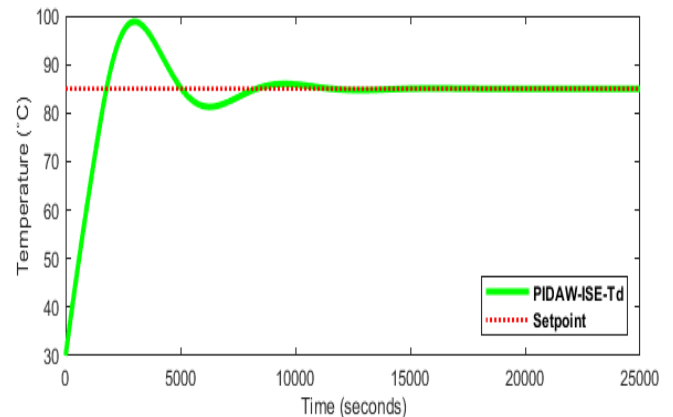


Fig. 5. Output for PIDAW-ISE- T_d Controllers in Step Test Simulation.

TABLE IV. SIMULATION RESPONSE ANALYSIS OF PID-ISE AND PIDAW-ISE CONTROLLERS WITH DIFFERENT TRACKING TIME CONSTANT, T_d

Controller Time, (seconds)	%OS	Rise Time, Tr (seconds)	Settling, Ts
PID-ISE	26.7211	2353	12684
PIDAW-ISE- T_i	17.7318	1405*	7814
PIDAW-ISE- $\sqrt{T_i T_d}$	16.8949	1405*	7784
PIDAW-ISE- T_d #	16.2615*	1405*	7773*

Note: # indicate for the best controller / * indicate the best performance by category.

IV. SIMULATION RESULTS FOR HFPID AND HFPIDAW CONTROLLERS

The step response on steam distillation process are applied to evaluate the controller performance based on the percentage of overshoot, rise time, settling time and matrices index as the numerical data are tabulated in Table V. And, Figure 6 illustrates the step response of HF-PIDAW-ISE- T_d -5MF. The HF-PID are developed by using two set of Membership Function (MF) which are 5MF and 7MF. Then, the modified version of HF-PID which enhancement with anti-windup strategy is also presented. The Table V shows the HF-PIDAW-ISE- $\sqrt{T_i T_d}$ -7MF recorded the lowest percentage of overshoot with 0.04% followed by the HF-PIDAW-ISE- T_i -5MF, HF-PIDAW-ISE- $\sqrt{T_i T_d}$ -5MF, HF-PIDAW-ISE- T_i -7MF, HF-PIDAW-ISE- T_d -5MF, HF-PIDAW-ISE- T_d -7MF, HF-PID-ISE-7MF and lastly HF-PID-ISE-5MF.

In term of rise time, HF-PID-ISE-5MF revealed the fastest rise time with 2261 seconds followed by the HF-PID-ISE-7MF, HF-PIDAW-ISE- T_d -5MF, HF-PIDAW-ISE- T_d -7MF, HF-PIDAW-ISE- T_i -5MF, HF-PIDAW-ISE- $\sqrt{T_i T_d}$ -5MF, HF-PIDAW-ISE- T_i -7MF, and lastly HF-PIDAW-ISE--7MF. $\sqrt{T_i T_d}$ The settling time criteria shown that HF-PIDAW- T_d ISE--7MF recorded the fastest settling time with 3887 seconds and followed by HF-PIDAW-IST $_d$ E--5MF, HF-PIDAW-ISE-- T_i 5MF, HF-PIDAW-ISE $\sqrt{T_i T_d}$ --5MF, HF-PIDAW-ISE- T_i -7MF, HF-PIDAW-ISE $\sqrt{T_i T_d}$ --7MF, HF-PID-ISE-7MF and lastly HF-PID-ISE-5MF.

After carefully putting into consideration the factor mentioned above, the HF-PIDAW-IST $_d$ E--5MF controller has better transient response compared to all HF-PID and HF-PIDAW control variance. Although the HF-PIDAW-IST $_d$ E--5MF is not selected the preferred performance in any category, the %OS is still lower which less than 1%, and second lower of rise time and settling time among the controllers.

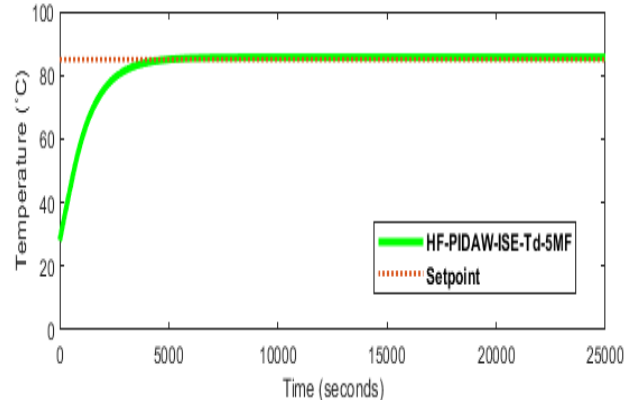


Fig. 6. Output for HF-PIDAW-ISE- T_d -5MF Controllers in Step Test Simulation.

TABLE V. SIMULATION RESPONSE ANALYSIS OF HF-PID VERSUS HF-PIDAW CONTROLLER

Controller Time, (seconds)	%OS	Rise Time, Tr (seconds)	Settling, Ts
HF-PID-ISE-5MF	2.00	2261*	26000
HF-PID-ISE-7MF	1.00	2340	8127
HF-PIDAW-ISE- T_i -5MF	0.29	2420	4160
HF-PIDAW-ISE- $\sqrt{T_i T_d}$	0.29	2421	4162
HF-PIDAW-ISE- T_d -5MF#	0.74	2367	3911
HF-PIDAW-ISE- T_i -7MF	0.41	2485	4189
HF-PIDAW-ISE- $\sqrt{T_i T_d}$ -7MF	0.04*	2556	4515
HF-PIDAW-ISE- T_d	0.89	2405	3887*

Note: # indicate for the best controller / * indicate the best performance by category

V. CONCLUSIONS

In this study, the simulation of HFPID based controller in regulating the temperature of the time-varying of steam distillation process has been benchmarked with PID based controller. The result has clearly revealed that the HFPID based controller namely HF-PIDAW significantly improved the temperature regulation of steam distillation process in term of providing fast settling time and minimal overshoot as compared

with the best performance of PID based controller. Overall, based on ranking, HF-PIDAW-ISE $-T_d$ -5MF is the most preferred controller in terms of producing better transient response, followed by others HF-PIDAW controllers consecutively.

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